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10/676,445

PATENT  
Conf. No.: 7027



IN THE UNITED STATES PATENT OFFICE

Applicants: Amit Singh et al.  
Assignee: Xilinx, Inc.  
Title: Pin Reordering During Placement of Circuit Designs  
Serial No.: 10/676,445 File Date: 09-30-03  
Examiner: Sun J. Lin Art Unit: 2825  
Docket No.: X-1494 US Conf. No.: 7027

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Mail Stop AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on September 19, 2005, please replace/substitute the following drawings and /claims as indicated.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Amendments to the Drawings** begin on page 8 of this paper and include a replacement sheet.

**Remarks** begin on page 9 of this paper.

An **Appendix** including an amended drawing figure (Figures 1A-B) is attached following page 10.